

# Curriculum Vitae

## Personal Information

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## Education

- PhD candidate (2005 - ), Department of Computer Science and Technology, University of Science and Technology of China, Supervisor: Prof. Yinlong Xu.
- BEng (2001 - 2005), Department of Computer Science and Technology, University of Science and Technology of China.

## Work Experience

- Dec. 2006 – Oct. 2007. Intern of the Microprocessor Technology Lab, Intel China Research Center, Beijing, China.
- Sep. 2004 – present. Student member of the National High Performance Computing Center, Hefei, China.

## Publications

1. **Qi Zhang**, Yurong Chen, Jianguo Li, Yimin Zhang, Yinlong Xu, “Parallelization and Performance Analysis of Video Feature Extractions on Multi-Core Based Systems”, In *Proc. of 36<sup>th</sup> International Conference on Parallel Processing (ICPP-07)*, Xi’an, China, Sep 2007.
2. Qi Zhang, Yurong Chen, Yimin Zhang, Yinlong Xu, “SIFT Implementation and Optimization for Multi-Core Systems”, accepted by the *10<sup>th</sup> Workshop on Advances in Parallel and Distributed Computational Models (APDCM-08)*, Miami, Florida USA, April, 2008.

## Teaching Experience

- Teaching assistant of Algorithm Design and High Performance Implementation.
- Teaching assistant of Multi-Core Parallel Computing.

## Honors and Awards

- "Outstanding Student Scholarship" every year from 2001 to 2004.